

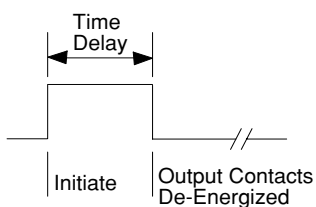


- Digital CMOS Design
- 10 Amp, DPDT
- $\pm 0.2\%$ Repeatability
- Transient Protected
- Timing Ranges Up To 24 Hours



Operation

Interval On
When input voltage is applied to the CDB, the internal relay is energized, transferring the output contacts, and the timing cycle begins. At the end of the timed period the internal relay is de-energized and the timing circuit is reset. Removal of input voltage during or after the timing cycle will de-energize the internal relay and reset the timing circuit.



Specifications

Electrical

Input Voltage:
24 or 115VAC, $\pm 10\%$, 50/60Hz
24 or 125VDC $\pm 10\%$, Filtered or Full Wave

Time Delays:

Type: Adjustable or Factory Fixed
Range: 50 Milliseconds to 24 Hours
Repeat Accuracy: $\pm 0.2\%$ of Time Range or ± 10 Milliseconds, Whichever is Greater.
Fixed Time Accuracy: $\pm 5\%$ Worst Case

Reset Times:

During Timing: 50 Milliseconds, Typical
After Timing: 50 Milliseconds, Typical
Protection: Varistor and/or R-C Network
Power Consumption: 5VA

Output Relay: 10 Amps @ 120/240VAC
500,000 Full Load Electrical Cycles
50,000,000 Mechanical Cycles

U.L. & CSA Ratings:

5 Amps, 1/3 HP, 125VA @ 240VAC
5 Amps, 1/6 HP, 125VA @ 120VAC

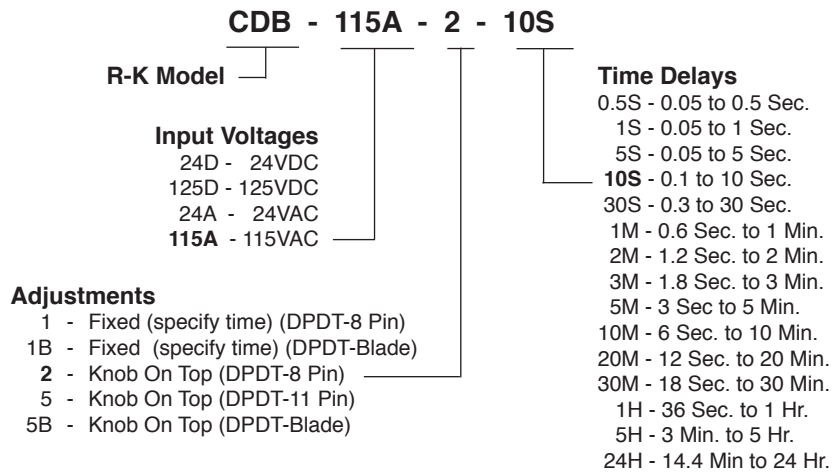
Physical

Mounting: Plug-In
Termination: 8 or 11 Pin & Blade Base
Packaging: Dust Cover
Weight: 7 Oz.

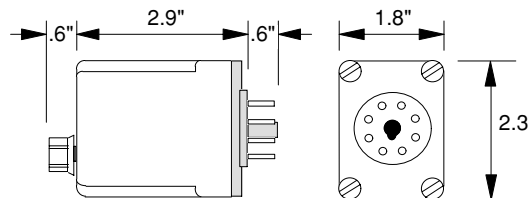
Ambient Temperatures

Operating: -10°C to 65°C
U.L. Operating: 0°C to 40°C
Storage: -10°C to 85°C

Ordering Information



Dimensions



Connections

